

ABSTRACT OF THE DISCLOSURE

A liquid crystal display panel includes a plurality of gate lines arranged along a first direction on a first substrate, a plurality of data lines arranged along a second direction on the first substrate to cross the gate lines to define a plurality of unit pixels, an insulating layer disposed over the gate and data lines, a common electrode disposed on a second substrate opposite to the first substrate, a plurality of pixel electrodes, each pixel electrode provided in each of the unit pixels partitioned by the gate line and the data line, and a plurality of side electrodes overlapping the data lines, wherein the insulating layer is provided between the side electrode and the data lines.